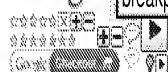




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### 1. CompactRISC CR16 FAQs

What is its typical **throughput**? CompactRISC uses a 3 **stage pipelines**. The **pipeline stages** are: ... debug support with single step and up to 16 hardware **breakpoints** on instruction, address or data matches. ... LM3530 - High Efficiency, Multi **Display** LED Driver with 128 Exponential Dimming Steps & Integrated OLED ...

[www.national.com/analog/compactrisc/faq](http://www.national.com/analog/compactrisc/faq) - [Cached](#) - [Similar](#)

### 2. Retiring early-completion instructions to improve computer ...

Computers commonly execute program instructions in a **pipeline** in which the system begins ... System **throughput** is thus limited by the processor's retirement bandwidth, ... In a first embodiment, at each **stage** of the various processing **stages**, .... the movement of data from input device 528 to **display** device 532. ...

[www.patentstorm.us/patents/6944752/description.html](http://www.patentstorm.us/patents/6944752/description.html)

### 3. HIGH THROUGHPUT PIPELINED DATA PATH - Patent application - Methods ...

2-3, the **pipeline** may include three **stages** in an embodiment. ... e.g., a **display**, a keyboard, and a mouse, and the like. .... (if it was just partial data) because the **break point** allowable discount boundary (ADB) occurs at that point. ...

[www.faqso.org/patents/app/20060234799](http://www.faqso.org/patents/app/20060234799) - [Cached](#) - [Similar](#)

### 4. High-throughput genotyping by whole-genome resequencing

The development of genomics concepts and tools has set the **stage** for replacing the .... When the sliding **window** hit a homozygous/homozygous **breakpoint**, .... base calling were performed using Illumina GA processing **pipeline** V0.2.2.6. ...

[www.pubmedcentral.nih.gov/articlerender.fcgi?artid=2694477](http://www.pubmedcentral.nih.gov/articlerender.fcgi?artid=2694477) - [Similar](#)

by X Huang - 2009 - [Cited by 1](#) - [Related articles](#)

### 5. [PDF] FPGA Implementation of a Three-Stage SMIPsv2 Processor

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Unlike the VCS or BlueSim simulators, where **\$display** and waveform viewers may ...  
If the **breakpoint** is enabled, then the processor should halt when the ... Identify the  
source of conflict between the rules implementing the three **pipeline stages** of your ...  
What **throughput** does this version of your design achieve? ...

[csg.csail.mit.edu/6.375/handouts/labs/lab3.pdf](http://csg.csail.mit.edu/6.375/handouts/labs/lab3.pdf) - [Similar](#)

#### 6. [An accelerator design for speedup of Java execution in consumer ...](#)

Jan 8, 2009 ... We make the register file as Java stack top **window**, so for the  
bytecodes .... Once all successor **pipeline stages** idle, the Java processor will send ...  
the **breakpoint**, the later **stages** of PC will continue till **pipeline** halts. .... [27] Singh M,  
Nowick S. High-throughput asynchronous **pipelines** for ...

[linkinghub.elsevier.com/retrieve/pii/S0045790608001341](http://linkinghub.elsevier.com/retrieve/pii/S0045790608001341) - [Similar](#)

by L Yan - 2009 - [Related articles](#)

#### 7. [High-Throughput Expression of C. elegans Proteins — Genome Research](#)

The **pipeline** described here is applicable to high-throughput expression of  
recombinant .... View larger version: In this **window** · In a new **window** .... The **break**  
**point** for the two panels was chosen for easy presentation. .... In a 1-L confirmation  
**stage** of the soluble candidates identified in a small-scale **screen**. ...

[genome.cshlp.org/content/14/10b/2102.full](http://genome.cshlp.org/content/14/10b/2102.full) - [Similar](#)

by CH Luan - 2004 - [Cited by 53](#) - [Related articles](#) - [All 15 versions](#)

#### 8. [High Throughput Screening Retools for the Future - Bio-IT World](#)

Has high-throughput screening (HTS) suffused drug discovery **pipelines** with "hits" ...  
and costly and embarrassing late-stage and post-market failures continue to plague ...  
In fact, HTS has done what it was designed to do, namely to **screen** .... double the  
workload, and a 20% **breakpoint** would quadruple the workload. ...

[www.bio-itworld.com/BioIT\\_Article.aspx?id=86636](http://www.bio-itworld.com/BioIT_Article.aspx?id=86636) - [Cached](#)

#### 9. [Shadow translation look-aside buffer and method of operation ...](#)

Therefore, the instruction **throughput** of an N **stage** pipelined architecture .... **display**  
904, and mass storage 906 through the PCI-, ISA-, and X-Buses. ... The IU 215  
comprises the following **pipeline stages**: Instruction Fetch (IF) .... For each  
**breakpoint** address in DR0-DR3, there are corresponding fields L, R/W, ...

[www.freepatentsonline.com/5946718.html](http://www.freepatentsonline.com/5946718.html)

by DW Green - 1999 - [Cited by 8](#) - [Related articles](#) - [All 2 versions](#)

#### 10. [\[PDF\] Pipeline](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Maximize vertex **throughput** using buffer objects. Just like in OpenGL 2. , an

application can map a buffer object ... well as **display** lists. Good progress was made defining what .... **stages** in the programmable graphics **pipeline**. There is no incre- ....

API function **breakpoints**: The **Breakpoint** dialog lets a devel- ...

[www.opengi.org/imgt/uploads/pipeline/pipeline\\_004.pdf](http://www.opengi.org/imgt/uploads/pipeline/pipeline_004.pdf) - [Similar](#)



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